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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Schloesser, et al. Docket No.: INF-135
Filed: 2/13/2004 Examiner: Andy Huynh
Serial No.: 10/777,128 Art Unit: 2818
Title: Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells

Mail Stop Issue Fee
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant wishes to bring to the attention of the U.S. Patent and Trademark Office the information noted on the enclosed PTO Form PTO/SB/08A, which may be considered material to the examination of the above-identified application. Copies of the U.S. Patents cited are not being submitted.

This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(d) together with a Statement under 37 C.F.R. §1.97(e), and a \$180.00 fee set forth in 37 C.F.R. §1.17(p) as this disclosure is being filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee.

Please charge the required fee of \$180.00 and any additional amount, or credit any overpayment to Deposit Acct. No 50-1065 of the below mentioned firm.

01/13/2006 RHEBRAHT 00000046 10777128

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Respectfully submitted,

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FACSIMILE COVER SHEET

To: Examiner Andy Huynh	Total Pages Sent: 5
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: 571-273-2885	Transmission Date: January 12, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Applicant: Schloesser, et al. Docket No.: INF-135
Serial No: 10/777,128 Art Unit: 2818
Date Filed: February 13, 2004
Title: Architecture for Vertical Transistor Cells and Transistor-Controlled Memory Cells

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-2885 on the date shown above:

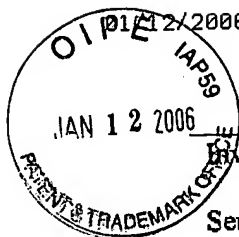
- Certification of Facsimile Transmission (1 page)
- Information Disclosure Statement (1 original and 1 copy = 2 pages)
- Statement Under 37 CFR 1.97(e) (1 page)
- Form PTO/SB/08A (1 page), with 2 references cited

Respectfully submitted,


Anne Marie James
Legal Assistant

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor(s): Schloesser, et al.

Serial No.: 10/777,128

Filing Date: 2/13/2004

Title: Architecture for Vertical Transistor
Cells and Transistor-Controlled Memory
Cells

Attorney Docket No.: INF-135

Examiner:

Andy Huynh

Group Art Unit:

2818

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COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

STATEMENT UNDER 37 CFR 1.97(e)

Sir:

The undersigned hereby certifies that either:

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- () No item of information contained in the Information Disclosure Statement
- was cited in a communication from a foreign patent office in a counterpart foreign application, and
 - to the knowledge of the undersigned, after making reasonable inquiry, was known to an individual designated in 37 CFR 1.56 (c) more than three months prior to the filing of the Information Disclosure Statement.

Respectfully Submitted,

Schloesser, et al.

By 

Ira S. Matsil

Attorney/Agent for Applicant(s)

Reg. No.: 35,272

I hereby certify that this Correspondence is being deposited with the United States Postal service with sufficient postage for first class mail in an envelope address to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, or the correspondence is being facsimile transmitted to the USPTO, on the date indicated below.

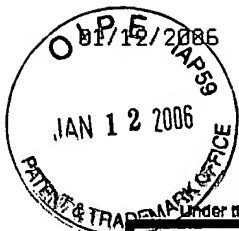
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Substitute for form 1449A/PTO		Complete if Known			
		Application Number	10/777,128		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Filing Date	2/13/2004		
		First Named Inventor	Schloesser, et al.		
		Art Unit	2818		
		Examiner Name	Andy Huynh		
Sheet	1	of	1	Attorney Docket Number	INF-135

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US-8,033,957	03-07-2000	Burns, Jr. et al.	
	2	US-2002/0109178 A1	08-15-2002	Forbes et al.	
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FOREIGN PATENT DOCUMENTS						
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